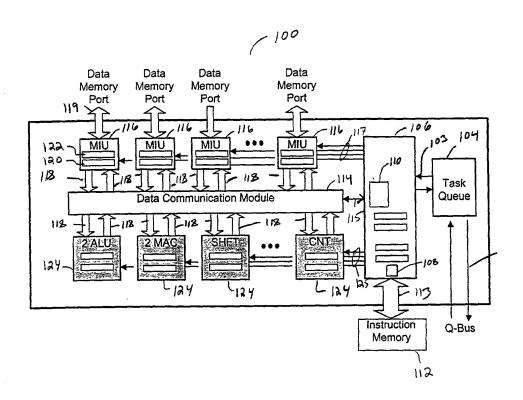
"Designer Configurable Multi-Processor

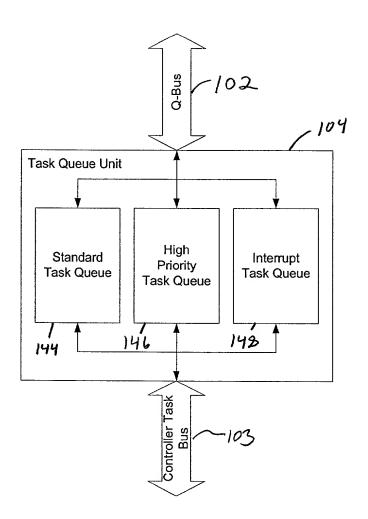
System" by Levia et al.

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F16.1

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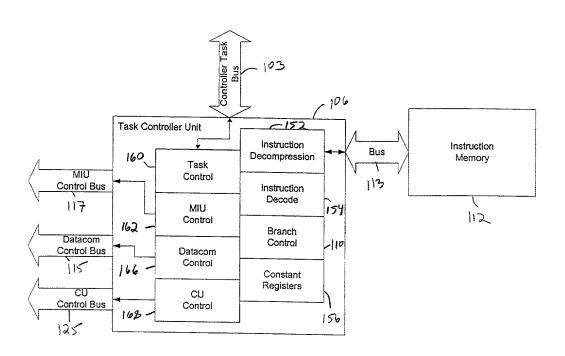


F16.2

"Designer Configurable Multi-Processor System" by Levia et al.

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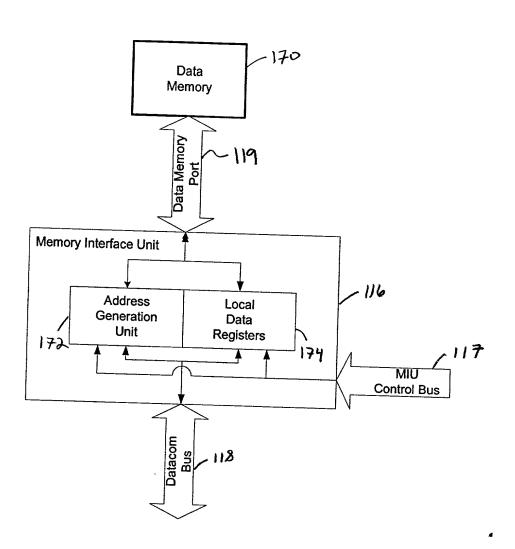


F16.3

"Designer Configurable Multi-Processor

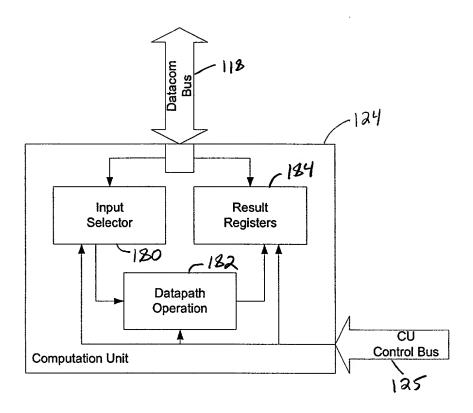
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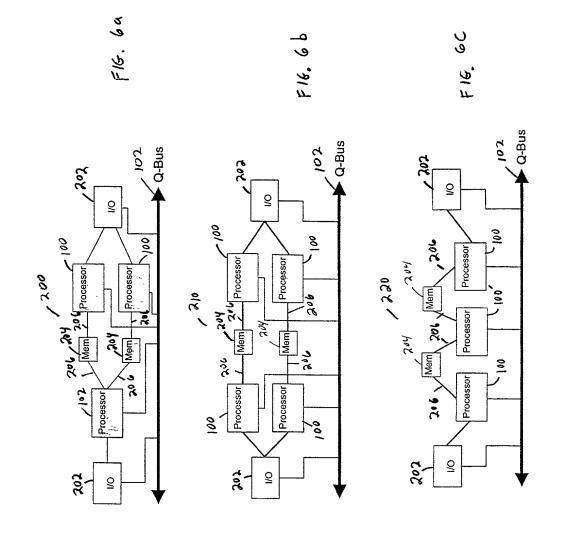


F16. 4

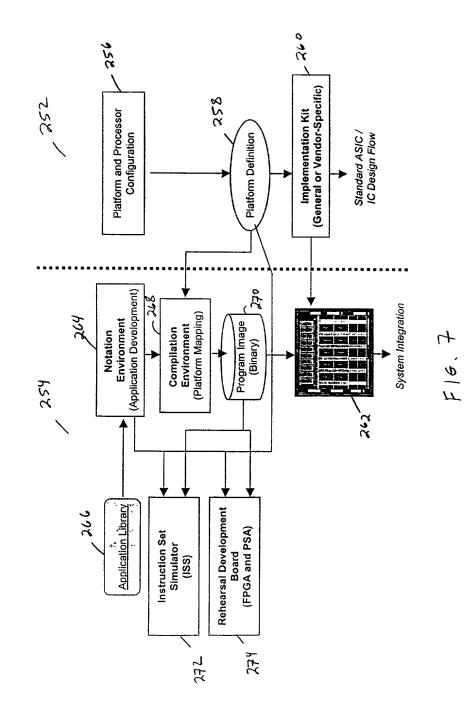
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F16. 5



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